## AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Previously Presented) A system design method for designing a system which includes a plurality of system components, the method comprising:

defining respective functional representations of the plurality of system components, each functional representation including at least one parameter value; and

automatically defining an allowable set of such parameter values in dependence upon the plurality of system components, wherein the allowable set of parameter values includes at least one common parameter values from the respective functional representations of at least two of the plurality of components.

2. (Original) A method as claimed in claim 1, wherein one of the system components is a bus.

3. (Original) A method as claimed in claim 2, wherein the functional representation of the bus includes a parameter value relating to bus width.

4. (Original) A method as claimed in claim 1, further comprising choosing an allowable set of parameter values and setting the parameter values of the functional representations concerned to the values defined by the chosen allowable set of parameter values.

5. (Original) A method as claimed in claim 1, further comprising the steps of:

selecting a plurality of system components;

selecting a connection for interconnecting such selected system components; and

selecting one of the allowable sets of parameter values, in dependence upon said connection.

6. (Cancelled).

7. (Cancelled).

8. (Cancelled).

9. (Previously Presented) Apparatus for designing a system which includes a plurality of system components, the apparatus comprising:

a data storage medium which is operable to store respective functional representations of a plurality of system components, each functional representation including at least one parameter value; and

a processor which is operable to define automatically an allowable set of parameter values for a selected group of system components, wherein the allowable set of parameter values includes at least one common parameter value from the respective functional representations of at least two of the plurality of the components.

10. (Original) Apparatus as claimed in claim 9, wherein one of the system components is a bus.

11. (Previously Presented) Apparatus as claimed in claim 10, wherein the functional representation of the bus includes a parameter value relating to bus width.

12. (Original) Apparatus as claimed in claim 9, wherein the processor is operable to choose an allowable set of parameter values and setting the parameter values of the functional representations concerned to the values defined by the chosen allowable set of parameter values.

13. (Original) Apparatus as claimed in claim 9, wherein the processor is operable to:

select a plurality of system components;

select a connection for interconnecting such selected system components; and

select one of the allowable sets of parameter values, in dependence upon said connection.

14. (Original) A programmable logic device designed in accordance with a method as claimed in claim 1.

15. (Original) A programmable logic device designed using apparatus as claimed in claim 9.